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Docket No.: 0951-0133P  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Kenji TAKASE

Application No.: 10/791,750

Confirmation No.: 005270

Filed: March 4, 2004

Art Unit: 2818

For: SEMICONDUCTOR DEVICE AND  
MANUFACTURING METHOD FOR SAME

Examiner: T. T. V. Ho

**REPLY TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the Restriction Requirement dated June 17, 2005, the following remarks are respectfully submitted in connection with the above-identified application.

REMARKS

Claims 1-20 are pending in the present application.

The Examiner has required election in the present application between:

Group I, claims 1-10 and 13-20, drawn to a semiconductor device, classified in class 257, subclass 660; and

Group II, claims 11-12, drawn to a method of making a semiconductor device, classified in class 438, subclass 48.

**For the purpose of examination of the present application, Applicants elect, without traverse, Group 1, Claims 1-10 and 13-20.**

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Scott Wakeman, Registration No 37,750 at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

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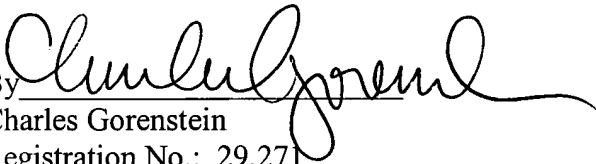
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- ☐ Attached is a Petition for Extension of Time.
- ☐ Attached hereto is the fee transmittal listing the required fees.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to our Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under § 1.17; particularly, extension of time fees.

Dated: July 7, 2005

Respectfully submitted,

By   
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